

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	149	memory with (test testing tester tested) and (feedback with path) and repair	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 01:52
L2	9	reload with (scan adj (path chain))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 01:53
S1	55977	memory with (test testing tester tested)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/04 16:59
S2	22252	(memory with (test testing tester tested)).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 11:18
S3	6371	memory with (test testing tester tested) and feedback	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 11:22
S4	751	memory with (test testing tester tested) and (feedback near3 path)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 11:23
S5	949	memory with (test testing tester tested) and (feedback with path)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/08 10:58
S6	0	memory with (test testing tester tested) and (feedback with path) and BISR	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 11:32
S8	100372	bit near3 (latch capacity storage register)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 11:37

EAST Search History

S9	482	(memory with (test testing tester tested) and (feedback with path)) and (bit near3 (latch capacity storage register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/08 13:35
S10	50	("6667917" "6067262" "5956350" "6367042" "6651202" "5920515" "6560740" "6304989" "5987632" "5909404" "6425103" "6505308" "4918378" "5006787" "6300769" "6505313" "6640321" "6373758" "5675545" "6065134" "6249893" "5862151" "5982681" "5425035" "5577050" "5258986" "5761215" "6185709" "5748543" "6085334" "6408401" "6691252" "6718496" "5968192" "6044481" "5471482" "5936876" "6018484" "6185138" "6151692" "6167544" "6510530" "6249889" "5825785" "5668947" "5724367" "5784382" "6341092" "6388930" "5640509").pn.	USPAT	OR	ON	2004/05/26 12:08
S11	2	"6691937".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 11:44
S12	3	flare adj register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 12:17
S13	11	flare near3 register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 19:36
S14	37	flare with register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 12:19
S15	3	flare with register and bisr	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 17:28

EAST Search History

S16	164	(scan adj (latch path chain)) with feedback	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 12:43
S17	1	((scan adj (latch path chain)) with feedback) and (("6667917" "6067262" "5956350" "6367042" "6651202" "5920515" "6560740" "6304989" "5987632" "5909404" "6425103" "6505308" "4918378" "5006787" "6300769" "6505313" "6640321" "6373758" "5675545" "6065134" "6249893" "5862151" "5982681" "5425035" "5577050" "5258986" "5761215" "6185709" "5748543" "6085334" "6408401" "6691252" "6718496" "5968192" "6044481" "5471482" "5936876" "6018484" "6185138" "6151692" "6167544" "6510530" "6249889" "5825785" "5668947" "5724367" "5784382" "6341092" "6388930" "5640509").pn.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 12:25
S18	138	(scan adj (path chain)) with feedback	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 13:57
S19	2	re\$load with (scan adj (path chain))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 01:53
S20	44	memory with (test testing tester tested) and (feedback with path) and repair and scan	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 14:04
S21	50	memory with (test testing tester tested) and ((feedback) same scan) and repair	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 14:05
S22	99	re\$load with scan	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 14:42

EAST Search History

S23	2475	linear adj feedback adj shift adj register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 17:29
S24	15202	parallel near2 load	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 17:29
S25	84	(linear adj feedback adj shift adj register) and (parallel near2 load)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 19:13
S26	329	zerbe.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 19:14
S27	35	zerbe.in. and jared.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 19:14
S28	3	PURI.in. and MUKESH.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/26 19:36
S29	53146	select near2 output	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/15 09:18
S30	2357	(select near2 output) and "714"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/15 09:18
S31	143	(select near2 output) and 714/718.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/08 10:58
S32	59320	memory with (test testing tested tester)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/15 09:34

EAST Search History

S33	3138	(select near2 output) and (memory with (test testing tested tester))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/15 09:34
S34	1836	(select near2 output) and (memory with (test testing tested tester)) and (MUX multiplexer multiplexor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/15 09:35
S35	1683	(select near2 output) and (memory with (test testing tested tester)) and ((MUX multiplexer multiplexor) with output)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/15 09:35
S36	1096	(select near2 output) and (memory with (test testing tested tester)) and ((MUX multiplexer multiplexor) with output) and shift	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/15 09:36
S37	1058	(select near2 output) and (memory with (test testing tested tester)) and ((MUX multiplexer multiplexor) with output) and shift and (sequence order)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/15 09:37
S38	851	(select near2 output) and (memory with (test testing tested tester)) and ((MUX multiplexer multiplexor) with output) and shift and (sequence order) and (return feedback)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/15 10:37
S39	824	(select near2 output) and (memory with (test testing tested tester)) and ((MUX multiplexer multiplexor) with output) and shift and (sequence order) and (return feedback) and register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/15 10:39
S40	171	(select near2 output) and 714/718. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/08 10:58
S41	5646057	(select selector selecting selected multiplexor multiplexer multiplexed multiplexing switch switching switched MUX)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/08 13:38

EAST Search History

S42	147000	(select selector selecting selected multiplexor multiplexer multiplxed multiplexing switch switching switched MUX) with register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/08 13:40
S43	71681	((select selector selecting selected multiplexor multiplexer multiplxed multiplexing switch switching switched MUX) same ((feedback shift output) near2 register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/08 13:41
S44	5281	((select selector selecting selected multiplexor multiplexer multiplxed multiplexing switch switching switched MUX) same ((feedback shift output) near2 register)) and (memory with (test testing tester tested))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/08 13:42
S45	5289	((select selector selecting selected multiplexor multiplexer multiplxed multiplexing switch switching switched MUX) same ((feedback shift output) near2 register)) and (memory with (test testing tester tested BIST BISR))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/08 13:44
S46	1824	((select selector selecting selected multiplexor multiplexer multiplxed multiplexing switch switching switched MUX) same ((feedback shift output) near2 register)) and (memory with (test testing tester tested BIST BISR)) and feedback	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/08 14:06
S47	1356	((select selector selecting selected multiplexor multiplexer multiplxed multiplexing switch switching switched MUX) same ((feedback shift output) near2 register)) and (memory with (test testing tester tested BIST BISR)) and (feedback same output)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/08 14:07
S48	506	((select selector selecting selected multiplexor multiplexer multiplxed multiplexing switch switching switched MUX) same ((feedback shift output) near2 register)) and (memory with (test testing tester tested BIST BISR)) and (feedback same output) and (serial\$3 near3 (shift scan))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/08 14:08

EAST Search History

S49	308	((select selector selecting selected multiplexor multiplexer multiplxed multiplexing switch switching switched MUX) same ((feedback shift output) near2 register)) and (memory with (test testing tester tested BIST BISR)) and (feedback same output) and ((serial\$3 near3 (shift scan)) with output)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/10 17:27
S50	1	whetsel.in. and harris.xa.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/10 17:06
S51	30	"806244"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/10 17:07
S52	800	(compress compression compressed signature) with (compare compared comparison comparing) with (expect\$4 expected expectation)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/10 17:09
S53	302	S52 and "714"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/10 17:10
S54	291	S52 and "714"/\$.ccls. and (test testing tested tester)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/10 17:10
S55	102	S52 and "714"/\$.ccls. and (test testing tested tester) and (detect detecting detection detector) and (correct correction corrected correcting)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/10 17:11
S56	32	((select selector selecting selected multiplexor multiplexer multiplxed multiplexing switch switching switched MUX) same ((feedback shift output) near2 register)) and (memory with (test testing tester tested BIST BISR)) and (feedback same output) and ((serial\$3 near3 (shift scan)) with output) and ((feedback same output) same ((serial\$3 near3 (shift scan)) with output))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/10 17:28

EAST Search History

S57	74203	((select selector selecting selected multiplexor multiplexer multiplexed multiplexing switch switching switched MUX) same ((shift\$4 scan output\$4) near2 register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 14:02
S58	74401	((select selector selecting selected multiplexor multiplexer multiplexed multiplexing switch switching switched MUX) same ((shift\$4 scan scanning output\$4) near2 register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 10:21
S59	51041	((select selector selecting selected multiplexor multiplexer multiplexed multiplexing switch switching switched MUX) with ((shift\$4 scan scanning output\$4) near2 register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 10:22
S60	14862	((select selector selecting selected multiplexor multiplexer multiplexed multiplexing switch switching switched MUX) same (((shift scan) near2 register) and (output near2 register)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 10:25
S61	562	((select selector selecting selected multiplexor multiplexer multiplexed multiplexing switch switching switched MUX) same (((shift scan) near2 register) and (output near2 register))) same feedback	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 13:31
S62	61	((select selector selecting selected multiplexor multiplexer multiplexed multiplexing switch switching switched MUX) same (((shift scan) near2 register) and (output near2 register))) same feedback) and ((test testing tester tested) with memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 13:31
S63	8	((select selector selecting selected multiplexor multiplexer multiplexed multiplexing switch switching switched MUX) same (((shift scan) near2 register) and (output near2 register))) and ((BISR (self adj repair)) with memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 13:36
S64	18	((select selector selecting selected multiplexor multiplexer multiplexed multiplexing switch switching switched MUX) and (((shift scan) near2 register) and (output near2 register))) and ((BISR (self adj repair)) with memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 13:42

EAST Search History

S65	10	S64 not S63	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 13:41
S66	42	((select selector selecting selected multiplexor multiplexer multiplxed multiplexing switch switching switched MUX) and (((shift scan) near2 register) and output)) and ((BISR (self adj repair)) with memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 13:42
S67	24	S66 not S64	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 13:53
S68	18	(britt harris).xa. and "714"/\$.ccor. and (parallel near2 (test testing))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 13:56
S69	62	(britt harris).xa. and "714"/\$.ccor. and parallel not whetsel.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 14:01
S70	609	parallel with mux with output	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 14:02
S72	34	((select selector selecting selected multiplexor multiplexer multiplxed multiplexing switch switching switched MUX) and output and parallel and (test testing tester) and register and strobe).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 14:03
S73	145	memory with (test testing tester tested) and (feedback with path) and repair	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/12 01:52
S74	42	((select selector selecting selected multiplexor multiplexer multiplxed multiplexing switch switching switched MUX) and (((shift scan) near2 register) and output)) and ((BISR (self adj repair)) with memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/12 11:03

EAST Search History

S75	42	((select selector selecting selected multiplexor multiplexer multiplxed multiplexing switch switching switched MUX) and (((shift scan) near2 register) and output)) and ((BISR (self adj repair)) with memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/12 11:21
S76	3	"4344155".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/13 12:36

Search History

Patent - FT

Set	Items	Description
S1	467508	REGISTER? ? OR LATCH OR LATCHES OR FLIP()FLOP? ?
S2	1824813	OUTPUT? ? OR OUT()PUT? ?
S3	1101686	SERIES OR SERIAL OR SERIALY OR SERIALI?ED
S4	22249	S1 (10N) (FEEDBACK? ? OR LOOP? ? OR LOOPEd OR LOOPING OR (P- UT OR PUTTING OR INPUT? OR PLACE? ? OR PLACING OR INSERT??
OR		INSERTING OR INSERTION? ? OR FEED OR FED OR FEEDING) ()BACK? ? OR RETURN?? OR RETURNING)
S5	5322	S2 (10N) S4
S6	355	S5 (7N) (SEQUENCE? ? OR SEQUENCING OR ORDER OR ORDERED
OR -		ORDERING)
S7	89	S5 (7N) (SEQUENTIALLY OR SERIALY OR SUCCESSION OR SUCCESS- IVELY)
S8	431	S6 OR S7
S9	23495	S1 (10N) S3
S10	68	S8 (30N) S9
S11	56	S10 AND AY=1963:2001
S12	56	IDPAT (sorted in duplicate/non-duplicate order)
S13	52	IDPAT (primary/non-duplicate records only)
S14	14	S11 AND IC=G01R
S15	14	IDPAT (sorted in duplicate/non-duplicate order)
S16	13	IDPAT (primary/non-duplicate records only)
S17	25	S11 AND IC=G06F
S18	16	S17 NOT S16
S19	16	IDPAT (sorted in duplicate/non-duplicate order)
S20	16	IDPAT (primary/non-duplicate records only)
S21	44397	SHIFT()S1 OR LFSR
S22	239	S8 (30N) S21
S23	15	S22 AND IC=G01R
S24	7	S23 NOT (S16 OR S20)
S25	7	IDPAT (sorted in duplicate/non-duplicate order)
S26	7	IDPAT (primary/non-duplicate records only)
File 348:EUROPEAN PATENTS 1978-2006/ 200640 (c) 2006 European Patent Office		
File 349:PCT FULLTEXT 1979-2006/UB=20061005UT=20060928 (c) 2006 WIPO/Thomson		
File 350:Derwent WPIX 1963-2006/UD=200664 (c) 2006 The Thomson Corporation		

Patent – Bib

Set	Items	Description
S1	145869	REGISTER? ? OR LATCH OR LATCHES OR FLIP()FLOP? ?
S2	1043519	OUTPUT? ? OR OUT()PUT? ?
S3	148598	SERIES OR SERIAL OR SERIALY OR SERIALI?ED
S4	2705	S1 (10N)(FEEDBACK? ? OR LOOP? ? OR LOOPED OR LOOPING OR (P-UT OR PUTTING OR INPUT? OR PLACE? ? OR PLACING OR INSERT?? OR INSERTING OR INSERTION? ? OR FEED OR FED OR FEEDING)()BACK? ? OR RETURN?? OR RETURNING)
S5	136	S4 (10N) (SEQUENCE? ? OR SEQUENCING OR SEQUENTIALLY OR ORDER OR ORDERED OR ORDERING OR SERIALY OR SUCCESSION OR SUCCESSIVELY OR CONSECUTIVE?)
S6	42	S5 (10N) S2
S7	39	S6 AND PY=1976:2001
S8	19	S7 AND IC=(G06F OR G01R)
S9	19	IDPAT (sorted in duplicate/non-duplicate order)
S10	19	IDPAT (primary/non-duplicate records only)

File 347:JAPIO Dec 1976-2006/Jan(Updated 061009)
(c) 2006 JPO & JAPIO

File 347:JAPIO Nov 1976-2004/Jul(Updated 041102)

(c) 2004 JPO & JAPIO

File 350:Derwent WPIX 1963-2004/UD,UM &UP=200471

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Set	Items	Description
S1	38512	SHIFT(1W)(REGISTER? ? OR REGISTRE? ?) OR SHIFTRREGIST? OR L- ATERAL(1W)CIRCUIT? OR LFSR? ?
S2	6761	(OUTPUT? OR OUT()PUT???? ? OR RESULT? ?)(1W)(REGISTER? ? OR REGISTRE? ?)
S3	1000470	ORDER? OR SEQUENCE?
S4	439155	IN()S3
S5	26433	S3(3N)(SAME OR IDENTICAL OR SELFSAME OR MAINTAIN? OR MAINT- ENANCE? OR PRESERV??? ? OR PRESERVATION OR KEEP??? ? OR RETAI- N??? ? OR RETENTION?)
S6	4020748	FEDBACK OR FEED??? ? OR FEEDBACK? OR FED OR TRANSFER???? ? OR XFER? OR SEND??? ? OR SENT OR TRANSMIT? OR TRANSMIS?
S7	2346099	EXCHANG??? ? OR RECEIPT OR RECEIVE OR RECEIVES OR RECEIVED OR RECEIVING OR DELIVER??? ? OR REDIRECT? OR RE()DIRECT??? ?
S8	1670275	ROUTE OR ROUTES OR ROUTED OR ROUTING OR MOVE OR MOVED OR M- OVES OR MOVING
S9	1285483	S6:S8(3N)(VALUE OR VALUES OR BIT OR BITS OR DATA OR INFORM- ATION OR SIGNAL? ? OR PULSE OR PULSES)
S10	18459	(OUTPUT? OR OUT()PUT???? ? OR RESULT? ?)(3W)(REGISTER? ? OR REGISTRE? ?)
S11	1520	RESEED? OR RE()SEED??? ? OR REFEED? OR REFED OR REFEDBACK?
S12	42	S11(3N)(VALUE OR VALUES OR BIT OR BITS OR DATA OR INFORMAT- ION OR SIGNAL? ? OR PULSE OR PULSES)
S13	6322	(S9 OR S12)(5N)S4:S5
S14	263	(S9 OR S12)(5N)SEQUENT?(3N)(SAME OR IDENTICAL OR SELFSAME - OR MAINTAIN? OR MAINTENAN? OR PRESERV??? ? OR PRESERVATION? OR KEEP??? ? OR RETAIN? OR RETENTION?)
S15	5180	S1 AND (S2 OR S10)
S16	13	S15 AND S13:S14
S17	1709	SEQUENT?(3N)(SAME OR IDENTICAL OR SELFSAME OR MAINTAIN? OR MAINTENAN? OR PRESERV??? ? OR PRESERVATION? OR KEEP??? ? OR R- ETAIN? OR RETENTION?)
S18	35132	(S6:S8 OR S11)(5N)(S4:S5 OR S17)
S19	18	S15 AND S18
S20	21	S16 OR S19
S21	21	IDPAT (sorted in duplicate/non-duplicate order)
S22	21	IDPAT (primary/non-duplicate records only)

22/9/2 (Item 2 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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013989579 **Image available**

WPI Acc No: 2001-473793/200151

**High speed de-interleaving device mixed with bit synchronization and
method thereof**

Patent Assignee: SEOUL MOBILE TELECOM (SEOU-N); SEOUL MOBILE TELECOM CORP
(SEOU-N)

Inventor: LEE S J; SHIN J Y; SIM J Y; SHIM J Y

Number of Countries: 001 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
KR 2001010002	A	20010205	KR 9928672	A	19990715	200151 B
KR 336290	B	20020513	KR 9928672	A	19990715	200273

Priority Applications (No Type Date): KR 9928672 A 19990715

File 348:EUROPEAN PATENTS 1978-2004/Oct W05

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File 349:PCT FULLTEXT 1979-2002/UB=20041104,UT=20041028

(c) 2004 WIPO/Univentio

Set	Items	Description
S1	17397	SHIFT(1W) (REGISTER? ? OR REGISTRE? ?) OR SHIFTRREGIST? OR L- ATERAL(1W)CIRCUIT? OR LFSR? ?
S2	6146	(OUTPUT? OR OUT()PUT???? ? OR RESULT? ?) (1W) (REGISTER? ? OR REGISTRE? ?)
S3	1068804	ORDER? OR SEQUENCE?
S4	890428	IN()S3
S5	145832	S3(3N) (SAME OR IDENTICAL OR SELFsame OR MAINTAIN? OR MAINT- ENANCE? OR PRESERV??? ? OR PRESERVATION OR KEEP??? ? OR RETAI- N??? ? OR RETENTION?)
S6	1228502	FEDBACK OR FEED??? ? OR FEEDBACK? OR FED OR TRANSFER???? ? OR XFER? OR SEND??? ? OR SENT OR TRANSMIT? OR TRANSMIS?
S7	1015548	EXCHANG??? ? OR RECEIPT OR RECEIVE OR RECEIVES OR RECEIVED OR RECEIVING OR DELIVER??? ? OR REDIRECT? OR RE()DIRECT??? ?
S8	693890	ROUTE OR ROUTES OR ROUTED OR ROUTING OR MOVE OR MOVED OR M- OVES OR MOVING
S9	428162	S6:S8(3N) (VALUE OR VALUES OR BIT OR BITS OR DATA OR INFORM- ATION OR SIGNAL? ? OR PULSE OR PULSES)
S10	25862	S9(5N)S4:S5
S11	1013	S1(25N)S2
S12	10	S11(25N)S10
S13	112742	S6:S8(5N)S4:S5
S14	979	S1(20N)S2
S15	18	S14(25N)S13
S16	10	S15 NOT S12
S17	10	IDPAT (sorted in duplicate/non-duplicate order)
S18	8	IDPAT (primary/non-duplicate records only)
S19	13056	(OUTPUT? OR OUT()PUT???? ? OR RESULT? ?) (3W) (REGISTER? ? OR REGISTRE? ?)
S20	3718	S1(25N)S19
S21	43	S20(25N)S13
S22	24	S20(25N)S10
S23	24	S21:S22 NOT (S12 OR S16)
S24	24	IDPAT (sorted in duplicate/non-duplicate order)
S25	24	IDPAT (primary/non-duplicate records only)
S26	2154	RESEED? OR RE()SEED??? ? OR REFEED? OR REFED OR REFEDBACK?
S27	24	S26(3N) (VALUE OR VALUES OR BIT OR BITS OR DATA OR INFORMAT- ION OR SIGNAL? ? OR PULSE OR PULSES)
S28	4965	SEQUENT?(3N) (SAME OR IDENTICAL OR SELFsame OR MAINTAIN? OR MAINTENAN? OR PRESERV??? ? OR PRESERVATION? OR KEEP??? ? OR R- ETAIN? OR RETENTION?)
S29	25917	(S9 OR S27) (5N) (S4:S5 OR S28)
S30	3653	S1(20N) (S2 OR S19)
S31	22	S30(25N)S29
S32	113085	(S6:S8 OR S26) (5N) (S4:S5 OR S28)
S33	40	S30(25N)S32
S34	0	S33 NOT (S12 OR S16 OR S25)

File 6:NTIS 1964-2004/Oct W5
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Set	Items	Description
S1	16008	SHIFT(1W) (REGISTER? ? OR REGISTRE? ?) OR SHIFTRIGIST? OR L- ATERAL(1W)CIRCUIT? OR LFSR? ?
S2	554	(OUTPUT? OR OUT()PUT???? ? OR RESULT? ?) (1W) (REGISTER? ? OR REGISTRE? ?)
S3	5618361	ORDER? OR SEQUENCE?
S4	1717111	IN()S3
S5	106866	S3(3N) (SAME OR IDENTICAL OR SELFSAME OR MAINTAIN? OR MAINT- ENANCE? OR PRESERV??? ? OR PRESERVATION OR KEEP??? ? OR RETAI- N??? ? OR RETENTION?)
S6	5895085	FEDBACK OR FEED??? ? OR FEEDBACK? OR FED OR TRANSFER???? ? OR XFER? OR SEND??? ? OR SENT OR TRANSMIT? OR TRANSMIS?
S7	3353251	EXCHANG??? ? OR RECEIPT OR RECEIVE OR RECEIVES OR RECEIVED OR RECEIVING OR DELIVER??? ? OR REDIRECT? OR RE()DIRECT??? ?
S8	1781862	ROUTE OR ROUTES OR ROUTED OR ROUTING OR MOVE OR MOVED OR M- OVES OR MOVING
S9	671319	S6:S8(3N) (VALUE OR VALUES OR BIT OR BITS OR DATA OR INFORM- ATION OR SIGNAL? ? OR PULSE OR PULSES)
S10	1001	(OUTPUT? OR OUT()PUT???? ? OR RESULT? ?) (3W) (REGISTER? ? OR REGISTRE? ?)
S11	6637	RESEED? OR RE()SEED??? ? OR REFEED? OR REFED OR REFEDBACK?
S12	142	S11(3N) (VALUE OR VALUES OR BIT OR BITS OR DATA OR INFORMAT- ION OR SIGNAL? ? OR PULSE OR PULSES)
S13	4322	(S9 OR S12) (5N) S4:S5

S14 8 (S9 OR S12) (5N) SEQUENT? (3N) (SAME OR IDENTICAL OR SELFSAME -
 OR MAINTAIN? OR MAINTENAN? OR PRESERV??? ? OR PRESERVATION? OR
 KEEP??? ? OR RETAIN? OR RETENTION?)
 S15 183 S1 AND (S2 OR S10)
 S16 0 S15 AND S13:S14
 S17 2777 SEQUENT? (3N) (SAME OR IDENTICAL OR SELFSAME OR MAINTAIN? OR
 MAINTENAN? OR PRESERV??? ? OR PRESERVATION? OR KEEP??? ? OR R-
 ETAIN? OR RETENTION?)
 S18 40442 (S6:S8 OR S11) (5N) (S4:S5 OR S17)
 S19 2 S15 AND S18
 S20 1 RD (unique items)
 ? t20/k

20/K/1 (Item 1 from file: 2)
 DIALOG(R) File 2:(c) 2004 Institution of Electrical Engineers. All rts.
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Abstract: **In order** to make a register **transfer** structure testable,
 it is usually divided into functional blocks that can be tested
 independently by...

... of each block. The test responses of a block are compacted or observed
 at its **output register** . In this paper a unified method for assembling
 all the single tests to a global...

...Descriptors: **shift registers**
 ?

File 9:Business & Industry(R) Jul/1994-2004/Nov 04
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File 13:BAMP 2004/Oct W3
(c) 2004 The Gale Group
File 16:Gale Group PROMT(R) 1990-2004/Nov 08
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File 47:Gale Group Magazine DB(TM) 1959-2004/Nov 08
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Set	Items	Description
S1	2428	SHIFT(1W) (REGISTER? ? OR REGISTRE? ?) OR SHIFTRREGIST? OR L- ATERAL(1W)CIRCUIT? OR LFSR? ?
S2	1088	(OUTPUT? OR OUT()PUT???? ? OR RESULT? ?) (1W) (REGISTER? ? OR REGISTRE? ?)
S3	6013503	ORDER? OR SEQUENCE?
S4	1704802	IN()S3
S5	144050	S3(3N) (SAME OR IDENTICAL OR SELFSAME OR MAINTAIN? OR MAINT- ENANCE? OR PRESERV??? ? OR PRESERVATION OR KEEP??? ? OR RETAI- N??? ? OR RETENTION?)
S6	5694541	FEDBACK OR FEED??? ? OR FEEDBACK? OR FED OR TRANSFER???? ? OR XFER? OR SEND??? ? OR SENT OR TRANSMIT? OR TRANSMIS?
S7	12515837	EXCHANG??? ? OR RECEIPT OR RECEIVE OR RECEIVES OR RECEIVED OR RECEIVING OR DELIVER??? ? OR REDIRECT? OR RE()DIRECT??? ?
S8	5617973	ROUTE OR ROUTES OR ROUTED OR ROUTING OR MOVE OR MOVED OR M- OVES OR MOVING
S9	1865114	S6:S8(3N) (VALUE OR VALUES OR BIT OR BITS OR DATA OR INFORM- ATION OR SIGNAL? ? OR PULSE OR PULSES)
S10	2275	(OUTPUT? OR OUT()PUT???? ? OR RESULT? ?) (3W) (REGISTER? ? OR REGISTRE? ?)
S11	2243	RESEED? OR RE()SEED??? ? OR REFEED? OR REFED OR REFEDBACK?
S12	14	S11(3N) (VALUE OR VALUES OR BIT OR BITS OR DATA OR INFORMAT- ION OR SIGNAL? ? OR PULSE OR PULSES)
S13	6880	(S9 OR S12) (5N)S4:S5
S14	25	(S9 OR S12) (5N)SEQUENT?(3N) (SAME OR IDENTICAL OR SELFSAME - OR MAINTAIN? OR MAINTENAN? OR PRESERV??? ? OR PRESERVATION? OR KEEP??? ? OR RETAIN? OR RETENTION?)
S15	70	S1(S) (S2 OR S10)
S16	0	S15(S)S13:S14
S17	1969	SEQUENT?(3N) (SAME OR IDENTICAL OR SELFSAME OR MAINTAIN? OR MAINTENAN? OR PRESERV??? ? OR PRESERVATION? OR KEEP??? ? OR R- ETAIN? OR RETENTION?)
S18	85621	(S6:S8 OR S11) (5N) (S4:S5 OR S17)
S19	0	S15(S)S18
?		

File 696:DIALOG Telecom. Newsletters 1995-2004/Nov 05
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File 15:ABI/Inform(R) 1971-2004/Nov 06
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File 647:CMP Computer Fulltext 1988-2004/Oct W5
(c) 2004 CMP Media, LLC
File 674:Computer News Fulltext 1989-2004/Sep W1
(c) 2004 IDG Communications

Set	Items	Description
S1	705	SHIFT(1W) (REGISTER? ? OR REGISTRE? ?) OR SHIFTRREGIST? OR L- ATERAL(1W)CIRCUIT? OR LFSR? ?
S2	450	(OUTPUT? OR OUT()PUT???? ? OR RESULT? ?) (1W) (REGISTER? ? OR REGISTRE? ?)
S3	6940974	ORDER? OR SEQUENCE?
S4	2057032	IN()S3
S5	191682	S3(3N) (SAME OR IDENTICAL OR SELFSAME OR MAINTAIN? OR MAINT- ENANCE? OR PRESERV??? ? OR PRESERVATION OR KEEP??? ? OR RETAI- N??? ? OR RETENTION?)
S6	6962596	FEDBACK OR FEED??? ? OR FEEDBACK? OR FED OR TRANSFER???? ? OR XFER? OR SEND??? ? OR SENT OR TRANSMIT? OR TRANSMIS?
S7	13103376	EXCHANG??? ? OR RECEIPT OR RECEIVE OR RECEIVES OR RECEIVED OR RECEIVING OR DELIVER??? ? OR REDIRECT? OR RE()DIRECT??? ?
S8	7523649	ROUTE OR ROUTES OR ROUTED OR ROUTING OR MOVE OR MOVED OR M- OVES OR MOVING
S9	1404942	S6:S8(3N) (VALUE OR VALUES OR BIT OR BITS OR DATA OR INFORM- ATION OR SIGNAL? ? OR PULSE OR PULSES)
S10	1130	(OUTPUT? OR OUT()PUT???? ? OR RESULT? ?) (3W) (REGISTER? ? OR REGISTRE? ?)
S11	4363	RESEED? OR RE()SEED??? ? OR REFEED? OR REFED OR REFEDBACK?
S12	61	S11(3N) (VALUE OR VALUES OR BIT OR BITS OR DATA OR INFORMAT-

ION OR SIGNAL? ? OR PULSE OR PULSES)
 S13 5395 (S9 OR S12) (5N) S4:S5
 S14 20 (S9 OR S12) (5N) SEQUENT? (3N) (SAME OR IDENTICAL OR SELFSAME -
 OR MAINTAIN? OR MAINTENAN? OR PRESERV??? ? OR PRESERVATION? OR
 KEEP??? ? OR RETAIN? OR RETENTION?)
 S15 14 S1(S) (S2 OR S10)
 S16 0 S15(S) S13:S14
 S17 2239 SEQUENT? (3N) (SAME OR IDENTICAL OR SELFSAME OR MAINTAIN? OR
 MAINTENAN? OR PRESERV??? ? OR PRESERVATION? OR KEEP??? ? OR R-
 ETAIN? OR RETENTION?)
 S18 90479 (S6:S8 OR S11) (5N) (S4:S5 OR S17)
 S19 0 S15(S) S18
 ?

File 347:JAPIO Nov 1976-2004/Jul(Updated 041102)
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 File 350:Derwent WPIX 1963-2004/UD,UM &UP=200471
 (c) 2004 Thomson Derwent
 File 348:EUROPEAN PATENTS 1978-2004/Oct W05
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 File 349:PCT FULLTEXT 1979-2002/UB=20041104,UT=20041028
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Set	Items	Description
S1	13	AU=PURI M?
S2	39	AU=AGRAWAL G?
S3	38	AU=CROSBY T?
S4	55909	SHIFT(1W)(REGISTER? ? OR REGISTRE? ?) OR SHIFTRREGIST? OR L- ATERAL(1W)CIRCUIT? OR LFSR? ?
S5	89	S1:S3
S6	1	S5 AND S4
S7	12907	(OUTPUT? OR OUT()PUT???? ? OR RESULT? ?)(1W)(REGISTER? ? OR REGISTRE? ?)
S8	0	S5 AND S7

? t6/9

6/9/1 (Item 1 from file: 350)
 DIALOG(R)File 350:Derwent WPIX
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016319823 **Image available**
 WPI Acc No: 2004-477718/200445
 XRPX Acc No: N04-376439

Fault e.g. control fault, testing method for dual port first-in-first-out memory device, involves executing read operations concurrently with write operation of data values into memory location set and at fixed memory addresses

Patent Assignee: LSI LOGIC CORP (LSIL-N)
 Inventor: IRRINKI V S; PURI M ; ZHAO J
 Number of Countries: 001 Number of Patents: 001
 Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 6757854	B1	20040629	US 99397542	A	19990916	200445 B

Priority Applications (No Type Date): US 99397542 A 19990916

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 6757854	B1	35	G11C-029/00	

Abstract (Basic): US 6757854 B1

NOVELTY - The method involves writing preset data values to two sets of memory locations. A series of read operations is performed to one memory location set. The read operations are executed concurrently with a write operation into another memory location set and at a fixed number of memory addresses away from the write operation. Each read operation is determined whether it returns a preset data value written into the former set.

USE - Used for testing fault e.g. control fault, **shift register** fault, full and empty flags fault, inter-port fault and reset/retransmit fault, in a pointer-based dual port first-in-first-out (claimed) memory device.

ADVANTAGE - The method utilizes a sequence of read and write operations, thereby efficiently and reliably detecting memory faults and avoiding the need to directly observe internal values within the first-in-first-out memory, such as the full and empty flag values and

the **shift register** values.

DESCRIPTION OF DRAWING(S) - DESCRIPTION OF DRAWING - The drawing shows a memory test circuit.

First-in-first-out (800)

Control logic (804)

Write register (808)

Write bus (812)

Read register (816)

pp; 35 DwgNo 8/8

Title Terms: FAULT; CONTROL; FAULT; TEST; METHOD; DUAL; PORT; FIRST; FIRST; MEMORY; DEVICE; EXECUTE; READ; OPERATE; CONCURRENT; WRITING; OPERATE; DATA; VALUE; MEMORY; LOCATE; SET; FIX; MEMORY; ADDRESS

Derwent Class: U13; U14

International Patent Class (Main): G11C-029/00

File Segment: EPI

Manual Codes (EPI/S-X): U13-C07; U14-A07; U14-A08; U14-D01B

?

[□ Search Result - Print Format](#)[< Back to Previous Page](#)

Key: IEEE JNL = IEEE Journal or Magazine, IEE JNL = IEE Journal or Magazine, IEEE CNF = IEEE Conference, IEE CNF = IEE Conference, IEEE STD = IEEE Standard

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2. **Linear complexity of a type of clock-controlled sequence**
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4. **A minimal hardware overhead BIST data compaction scheme**
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11. On the complexity of nonlinear nonbinary sequences

Lam, M.S.; Komo, J.J.;

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IEEE CNF

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